



M4i.66xx-x8 - 16 bit 1.25 GS/s Arbitrary Waveform Generator

- Fast 16 bit arbitrary waveform generator
- One, two or four channels
- Versions with 1.25 GS/s and 625 MS/s
- Ouput signal bandwidth up to 400 MHz
- Simultaneous signal generation on all channels
- Output level ± 80 mV to ± 2.5 V (± 2.0 V) into 50 Ω (± 160 mV to ± 5 V (± 4 V) into high-impedance loads)
- Fixed trigger to output delay
- Ultra Fast PCI Express x8 Gen 2 interface
- Huge 2 GSample on-board memory
- FIFO mode continuous streaming output
- Modes: Single-Shot, Loop, FIFO, Sequence Replay Mode, Gated, ...
- Two trigger input/output with AND/OR functionality
- Synchronization of up to 8 cards per system



- PCle x8 Gen 2 Interface
- Works with x8/x16* PCle slots
- Sustained streaming mode more than 1.5 GB/s

Operating Systems

- Windows XP, Vista, 7, 8, 10
- Linux Kernel 2.6, 3.x, 4.x
- Windows/Linux 32 and 64 bit

Recomended Software

- Visual Basic, Visual C++, Borland C++, GNU C++, Borland Delphi, VB.NET, C#, J#, Python
- SBench 6

<u>Drivers</u>

- MATLAB
- LabVIEW
- LabWindows/CVI

Model	Bandwidth	1 channel	2 channels	4 channels
M4i.6630-x8	400 MHz	1.25 GS/s		
M4i.6631-x8	400 MHz	1.25 GS/s	1.25 GS/s	
M4i.6620-x8	200 MHz	625 MS/s		
M4i.6621-x8		625 MS/s	625 MS/s	
M4i.6622-x8	200 MHz	625 MS/s	625 MS/s	625 MS/s

General Information

The M4i.66xx-x8 series arbitrary waveform digitizers deliver the highest performance in both speed and resolution. The series includes PCIe cards with either one, two or four synchronous channels. The large onboard memory can be segmented to replay different waveform sequences.

The AWG features a PCI Express x8 Gen 2 interface that offers outstanding data streaming performance. The interface and Spectrum's optimized drivers enable data transfer rates in excess of 1.5 GB/s so that signals can be continuously replayed at a high output rate.

While the cards have been designed using the latest technology they are still software compatible with the drivers from earlier Spectrum waveform generator cards. So, existing customers can use the same software they developed for a 10 year old 20 MS/s AWG card and for an M4i series 1.25 GS/s AWG.

^{*}Some x16 PCIe slots are for the use of graphic cards only and can not be used for other cards.

Software Support

Windows drivers

The cards are delivered with drivers for Windows XP, as well as Vista, Windows 7, Windows 8 and Windows 10 (each 32 bit and 64 bit). Programming examples for Visual C++, Borland C++ Builder, LabWindows/CVI, Borland Delphi, Visual Basic, VB.NET, C#, J#, Python and IVI are included.

Linux Drivers



All cards are delivered with full Linux support. Pre compiled kernel modules are included for the most common distributions like RedHat, Fedora, Suse, Ubuntu LTS or Debian. The Linux support includes SMP systems, 32 bit and 64 bit systems, versatile programming examples for Gnu

C++ as well as the possibility to get the driver sources for your own compilation.

SBench 6



A base license of SBench 6, the easyto-use graphical operating software for Spectrum cards, is included in the delivery. The base license makes it is possible to test the card, generate simple signals or load and replay previously stored SBench 6 signals. It's a valuable tool for checking the cards performance and assisting

with the units initial setup. The cards also come with a demo license for the SBenchó professional version. This license gives the user the opportunity to test the additional features of the professional version with their hardware. The professional version contains several advanced measurement functions, such as FFTs and X/Y display, import and export utilities as well as support for all replay modes including data streaming. Data streaming allows the cards to continuously replay data and transfer it directly from the PC RAM or hard disk. SBench 6 has been optimized to handle data files of several GBytes. SBench 6 runs under Windows as well as Linux (KDE and GNOME) operating systems. A test version of SBench 6 can be downloaded directly over the internet and can run the professional version in a simulation mode without any hardware installed. Existing customers can also request a demo license for the professional version from Spectrum. More details on SBench 6 can be found in the SBench 6 data sheet.

Third-party products

Spectrum supports the most popular third-party software products such as LabVIEW, MATLAB or LabWindows/CVI. All drivers come with detailed documentation and working examples are included in the delivery. Support for other software packages, like VEE or DasyLab, can also be provided on request.

Hardware features and options

PCI Express x8



The M4i series cards use a PCI Express x8 Gen 2 connection. They can be used in PCI Express x8 and x16 slots with Gen 1, Gen 2 or Gen 3. The maximum sustained data transfer rate is more than 3 GByte/s

(read direction) or 1.5 GByte/s (write direction) per slot. Server motherboards often recognize PCI Express x4 connections in x8 slots. These slots can also be used with the M4i series cards but with reduced data transfer rates.

Connections

- The cards are equipped with SMA connectors for the analog signals as well as for the external trigger and clock input. In addition, there are five MMCX connectors that are used for an additional trigger input, a clock output and three multi-function I/O connectors. These multi-function connectors can be individually programmed to perform different functions:
- Trigger output
- Status output (armed, triggered, ready, ...)
- Synchronous digital inputs, beeing stored inside the analog data samples
- Asynchronous I/O lines

Singleshot output

When singleshot output is activated the data of the on-board memory is played exactly one time. The trigger source can be either one of the external trigger inputs or the software trigger. After the first trigger additional trigger events will be ignored.

Repeated output

When the repeated output mode is used the data of the on-board memory is played continuously for a programmed number of times or until a stop command is executed. The trigger source can be either one of the external trigger inputs or the software trigger. After the first trigger additional trigger events will be ignored.

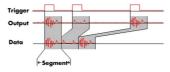
Single Restart replay

When this mode is activated the data of the on-board memory will be replayed once after each trigger event. The trigger source can be either the external TTL trigger or software trigger.

FIFO mode

The FIFO mode is designed for continuous data transfer between PC memory or hard disk and the generation board. The control of the data stream is done automatically by the driver on an interrupt request basis. The complete installed on-board memory is used for buffering data, making the continuous streaming extremely reliable.

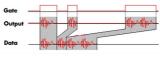
Multiple Replay



The Multiple Replay mode allows the fast output generation on several trigger events without restarting the hardware. With this option very fast repetition rates can be

achieved. The on-board memory is divided into several segments of the same size. Each segment can conatin different data which will then be played with the occurrence of each trigger event.

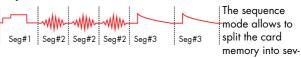
Gated Replay



The Gated Sampling mode allows data replay controlled by an external gate signal. Data is only replayed if the gate signal has attained a

programmed level.

Sequence Mode



eral data segments of different length. These data segements are

chained up in a user chosen order using an additional sequence memory. In this sequence memory the number of loops for each segment can be programmed and trigger conditions can be defined to proceed from segment to segment. Using the sequence mode it is also possible to switch between replay waveforms by a simple software command or to redefine waveform data for segments simultaneously while other segments are being replayed.

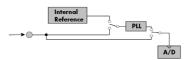
External trigger input

All boards can be triggered using up to two external analog or digital signals. One external trigger input has two analog comparators that can define an edge or window trigger, a hysteresis trigger or a rearm trigger. The other input has one comparator that can be used for standard edge and level triggers.

External clock input and output

Using a dedicated connector a sampling clock can be fed in from an external system. Additionally it's also possible to output the internally used sampling clock an a separate connector to synchronize external equipment to this clock.

Reference clock



The option to use a precise external reference clock (normally 10 MHz) is necessary to synchronize the instrument for high-quality

measurements with external equipment (like a signal source). It's also possible to enhance the quality of the sampling clock in this way. The driver automatically generates the requested sampling clock from the fed in reference clock.

Star-Hub



The star-hub is an additional module allowing the phase stable synchronisation of up to 8 boards of a kind in one system. Independent of the number of boards there is no phase delay between all channels. The star-hub distributes trigger and clock information between all boardsto ensure all connected boards are running with the same clock and trigger. All trigger

sources can be combined with a logical OR allowing all channels of all cards to be the trigger source at the same time.

Technical Data

Analog Outputs

Resolution 16 bit D/A Interpolation no interpolation

Output amplitude M4i.663x (1.25 GS/s version) software programmable ± 80 mV up to ± 2 V in 1 mV steps into 50 Ω termination (resulting in ± 160 mV up to ± 4 V in 2mV steps into high impedance loads)

 ± 80 mV up to ± 2.5 V in 1 mV steps into 50 Ω termination (resulting in ± 160 mV up to ± 5 V in 2mV steps into high impedance loads) Output amplitude M4i.662x (625 MS/s version) software programmable

Output offset

Output Amplifier Path Selection automatically by driver Low Power path: ± 80 mV to ± 480 mV (into $50~\Omega$) High Power path: ± 420 mV to ± 2.5 V/ ± 2 V (into $50~\Omega$)

420 mV to 480 mV (if output is using low power path it will switch to high power path at 480 mV. If output is using high power path it will switch to low power path at 420 mV) Output Amplifier Setting Hysteresis automatically by driver

TBD

Output amplifier path switching time 10 ms (output disabled while switching)

Filters software programmable bypass with no filter or one fixed filter DAC Differential non linearity (DNL) DAC only ±0.5 LSB typical

±1.0 LSB typical DAC Integral non linearity (INL) DAC only Output resistance 50 Ω Minimum output load 0Ω

 \pm 2.5 V for 625 MS/s versions or \pm 2 V for 1.25 GS/s version Max output swing in 50 Ω TRD

Crosstalk @ 1 MHz signal ±2.5 V into 50 Ω Output accuracy

Trigger

Available trigger modes External, Software, Window, Re-Arm, Or/And, Delay software programmable

software programmable Rising edge, falling edge or both edges Trigger edge 0 to (8GSamples - 32) = 8589934560 Samples in steps of 32 samples Trigger delay software programmable

Multi, Gate: re-arming time 40 samples Trigger to Output Delay 244 sample clocks (fixed)

Memory depth software programmable 32 up to [installed memory / number of active channels] samples in steps of 32 Multiple Replay segment size 16 up to [installed memory / 2 / active channels] samples in steps of 16 software programmable

External trigger accuracy 1 sample Minimum external trigger pulsewidth

≥ 2 samples

External trigger Ext0 Ext1 External trigger impedance software programmable $50 \Omega / 1 k\Omega$ $1 k\Omega$ External trigger coupling software programmable AC or DC fixed DC

External trigger type Window comparator Single level comparator

±10 V External input level $\pm 10 \text{ V } (1 \text{ k}\Omega), \pm 2.5 \text{ V } (50 \Omega),$

External trigger sensitivity (minimum required signal swing) 2.5% of full scale range 2.5% of full scale range = 0.5 V

±10 V in steps of 1 mV External trigger level ±10 V in steps of 1 mV software programmable

+30 V External trigger maximum voltage ±30V DC to 200 MHz / 150 MHz External trigger bandwidth DC 50 Ω /1 kΩ DC to 200 MHz

External triager bandwidth AC 20 kHz to 200 MHz 50 O n a

Clock

Clock Modes software programmable internal PLL, external reference clock, sync

Internal clock accuracy ≤ ±20 ppm Internal clock setup granularity 1 Hz Setable Clock speeds TBD

External reference clock range software programmable \geq 10 MHz and \leq 1 GHz External reference clock input impedance software programmable 50Ω fixed

External reference clock input coupling AC coupling External reference clock input edge Rising edge External reference clock input type Single-ended, sine wave or square wave

External reference clock input swing 0.3 V peak-peak up to 3.0 V peak-peak

External reference clock input max DC voltage ±30 V (with max 3.0 V difference between low and high level)

External reference clock input duty cycle requirement 45% to 55%

External reference clock output type Single-ended, 3.3V LVPECL Clock output

Star-Hub synchronization clock modes software selectable Internal clock, external reference clock

Sequence Replay Mode (Mode available starting with firmware V1.14)

Number of sequence steps software programmable 1 up to 4096 (sequence steps can be overloaded at runtime) 2 up to 64k (segment data can be overloaded at runtime) Number of memory segments software programmable

Loop Count software programmable 1 to (1M - 1) loops Sequence Step Commands software programmable Loop for #Loops, Next, Loop until Trigger, End Sequence

Special Commands software programmable Data Overload at runtime, sequence steps overload at runtime,

readout current replayed sequence step

Multi Purpose I/O lines (front-plate)

Number of multi purpose lines three, named X0, X1, X2 Input: available signal types software programmable Asynchronous Digital-In

Input: impedance 10 k Ω to 3.3 V Input: maximum voltage level -0.5 V to +4.0 V Input: signal levels 3.3 V LVTTL

Asynchronous Digital-Out, Synchronous Digital-Out, Trigger Output, Run, Arm, Sample Clock, Marker Output Output: available signal types software programmable

Output: impedance 50 Ω Output: signal levels 3.3 V LVTTL Output: type

 $3.3\mbox{V}$ LVTTL, TTL compatible for high impedance loads Output: drive strength Capable of driving 50 Ω loads, maximum drive strength ±48 mA

Connectors

Analog Inputs/Analog Outputs Cable-Type: Cab-3mA-xx-xx SMA female (one for each single-ended input) Trigger 0 Input SMA female Cable-Type: Cab-3mA-xx-xx Clock Input SMA female Cable-Type: Cab-3mA-xx-xx MMCX female Trigger 1 Input Cable-Type: Cab-1 m-xx-xx Clock Output MMCX female Cable-Type: Cab-1 m-xx-xx Multi Purpose I/O MMCX female (3 lines) Cable-Type: Cab-1 m-xx-xx

Environmental and Physical Details

Dimension (Single Card) 241 mm (3 4 PCle length) x 107 mm x 20 mm (single slot width) Dimension (Card with option SH8tm installed) 241 mm (3/4 PCIe length) x 107 mm x 40 mm (double slot width) Dimension (Card with option SH8ex installed) 312 mm (full PCle length) x 107 mm x 20 mm (single slot width)

Width (Standard and option SH8Ex) 1 slot

Width (option SH8tm installed) 2 slots Weight (M4i.44xx series) 4 channel version 290 g Weight (M4i.22xx and M4i.66xx series) 4 channel version 420 g Weight (Option star-hub -sh8ex, -sh8tm) including 8 sync cables 130 g Warm up time

10 minutes 0°C to 50°C Operating temperature -10°C to 70°C Storage temperature Humidity 10% to 90%

PCI Express specific details

x8 Generation 2 PCIe slot type PCle slot compatibility (physical) x8/x16

PCle slot compatibility (electrical) x1, x4, x8, x16, Generation 1, Generation 2, Generation 3

Certification, Compliance, Warranty

EMC Immunity Compliant with CE Mark EMC Emission Compliant with CE Mark

Product warranty 2 years starting with the day of delivery

Life-time, free of charge Software and firmware updates

Power Consumption

		PCI EXPRESS		
		3.3V	12 V	Total
M4i.6620-x8	Typical values: All channels activated, Sample rate: 625 MSps	0.2 A	2.5 A	31 W
M4i.6621-x8	Output signal: 31,25 MHz sine wave, Ouput level: +/- 1 V into 50 Ω load	0.2 A	2.7 A	33 W
M4i.6622-x8		0.2 A	3.0 A	36 W
M4i.6620-x8	Typical values: All channels activated, Sample rate: 625 MSps	0.2 A	2.6 A	32 W
M4i.6621-x8	Output signal: 31,25 MHz sine wave, Ouput level: +/- 2.5 V into 50 Ω load	0.2 A	2.9 A	35 W
M4i.6622-x8		0.2 A	3.3 A	40 W
M4i.6630-x8	Typical values: All channels activated, Sample rate: 1.25 GSps	0.2 A	2.7 A	33 W
M4i.6631-x8	Output signal: 31,25 MHz sine wave, Ouput level: +/- 1 V into 50 Ω load	0.2 A	3.0 A	36 W
M4i.6630-x8	Typical values: All channels activated, Sample rate: 1.25 GSps	0.2 A	2.9 A	35 W
M4i.6631-x8	Output signal: 31,25 MHz sine wave, Ouput level: +/- 2.0 V into 50 Ω load	0.2 A	3.3 A	40 W

MTBF

MTBF TBD

Bandwidth and Slewrate

	Filter	Output Amplitude	Mi4.6630-x8 M4i.6631-x8	M4i.6620-x8 M4i.6621-x8 M4i.6622-x8
Maximum Output Rate			1.25 GS/s	625 MS/s
-3d Bandwidth	no Filter	±480 mV	400 MHz	200 MHz
-3d Bandwidth	no Filter	±1000 mV	320 MHz	200 MHz
-3d Bandwidth	no Filter	±2000 mV	320 MHz	200 MHz
-3d Bandwidth	Filter	all	65 MHz	65 MHz
Slewrate	no Filter	±480 mV	2.25 V/ns	4.5 V/ns

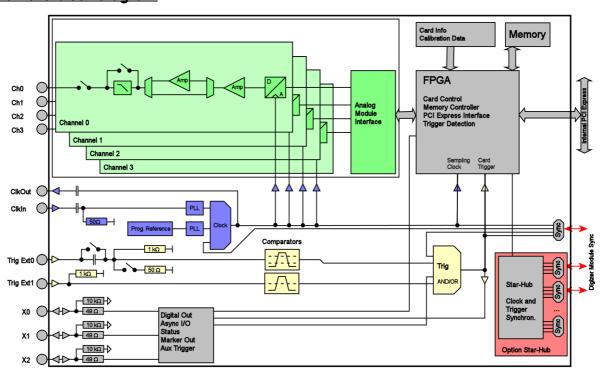
Dynamic Parameters

	M4i.6620-×8 M4i.6621-×8 M4i.6622-×8							
Test - Samplerate		625 MS/s		625 MS/s		625 MS/s		
Output Frequency		10 MHz		50 MHz		50 MHz		
Output Level in 50Ω	Output Level in 50 Ω ±480 mV ±1000mV ±25		±2500mV	±480 mV	±2500mV	±480 mV	±2500mV	
Used Filter		none			none		Filter enabled	
NSD (typ)	-150 dBm/Hz	-149 dBm/Hz	-149 dBm/Hz	-150 dBm/Hz	-149 dBm/Hz	-150 dBm/Hz	-149 dBm/Hz	
SNR (typ)	70.7 dB	72.4 dB	63.1 dB	65.3 dB	64.4 dB	67.5 dB	69.4 dB	
THD (typ)	-73.3 dB	-70.5 dB	-49.7 dB	-64.1 dB	-39.1 dB	-68.4 dB	-50.4 dB	
SINAD (typ)	69.0 dB	67.7 dB	49.5 dB	61.6 dB	39.1 dB	64.9 dB	50.3 dB	
SFDR (typ), excl harm.	98 dB	98 dB	99 dB	86 dB	76 dB	88 dB	89 dB	
ENOB (SINAD)	11.2	11.0	8.0	10.0	6.2	10.5	8.1	
ENOB (SNR)	11.5	11. <i>7</i>	10.2	10.5	10.4	10.9	11.2	

		M4i.6630-x8 M4i.6631-x8					
Test - Samplerate		1.25 GS/s		1.25 GS/s		1.25 GS/s	
Output Frequency		10 MHz		50 MHz		50 MHz	
Output Level in 50 Ω ±480 mV ±1000mV ±2000mV		±480 mV	±2000mV	±480 mV	±2000mV		
Used Filter		none		none		Filter enabled	
NSD (typ) -150 dBm/Hz -149 dBm/Hz -149 d		-149 dBm/Hz	-150 dBm/Hz	-149 dBm/Hz	-150 dBm/Hz	-149 dBm/Hz	
SNR (typ)	70.5 dB	72.1 dB	71.4 dB	65.2 dB	65.0 dB	67.2 dB	68.2 dB
THD (typ)	-74.5 dB	-73.5 dB	-59.1 dB	-60.9 dB	-43.9 dB	-67.9 dB	-63.1 dB
SINAD (typ)	69.3 dB	69.7 dB	59 dB	59.5 dB	43.9 dB	64.5 dB	61.9 dB
SFDR (typ), excl harm.	96 dB	97 dB	98 dB	85 dB	84 dB	87 dB	87 dB
ENOB (SINAD)	11.2	11.2	9.5	9.6	6.9	10.4	10.0
ENOB (SNR)	11.5	11.5	11.5	10.5	10.5	10.9	11.0

THD and SFDR are measured at the given output level and 50 Ohm termination with a high resolution M3i.4860/M4i.4450-x8 data acquisition card and are calculated from the spectrum. Noise Spectral Density is measured with built-in calculation from an HP E4401B Spectrum Analyzer. All available D/A channels are activated for the tests. SNR and SFDR figures may differ depending on the quality of the used PC. NSD = Noise Spectral Density, THD = Total Harmonic Distortion, SFDR = Spurious Free Dynamic Range.

Hardware block diagram



Order Information

The card is delivered with 2 GSample on-board memory and supports standard replay, FIFO replay (streaming), Multiple Replay, Gated Replay, Continuous Replay (Loop), Single-Restart as well as Sequence. Operating system drivers for Windows/Linux 32 bit and 64 bit, examples for C/C++, LabVIEW (Windows), MATLAB (Windows and Linux), LabWindows/CVI, IVI, .NET, Delphi, Visual Basic, Python and a Base license of the oscilloscope software SBench 6 are included.

Adapter cables are not included. Please order separately!

PCI Express x8	Order no.	Bandwidt	n Standard men	n 1 channel	2 channels	4 channels			
-	M4i.6620-x8	200 MHz	2 GSample	625 MS/s					
	M4i.6621-x8	200 MHz	2 GSample	625 MS/s	625 MS/s				
	M4i.6622-x8	200 MHz	2 GSample	625 MS/s	625 MS/s	625 MS/s			
	M4i.6630-x8	400 MHz	2 GSample	1.25 GS/s					
	M4i.6631-x8	400 MHz	2 GSample	1.25 GS/s	1.25 GS/s				
Options	Order no.	Option							
	M4i.xxxx-SH8ex (1)	Synchronization Star-Hub for up to 8 cards (extension), only one slot width, extension of the card to full PCI Express length (312 mm). 8 synchronization cables included.							
	M4i.xxxx-SH8tm (1)	Synchronization Star-Hub for up to 8 cards (top mount), two slots width, top mounted on card. 8 synchronization cables included.							
	M4i-upgrade	Upgrade	for M4i.xxxx: Later i	nstallation of option :	Star-Hub				
Standard Cables			Order no.						
	for Connections	Length	to BNC male	to BNC female	to SMA male	to SMA female	to SMB female		
	Analog/Clock-In/Trig-In	80 cm	Cab-3mA-9m-80	Cab-3mA-9f-80					
	Analog/Clock-In/Trig-In	200 cm	Cab-3mA-9m-200	Cab-3mA-9f-200					
	Clk-Out/Trig-Out/Extra	80 cm	Cab-1 m-9 m-80	Cab-1 m-9f-80	Cab-1m-3mA-80	Cab-1 m-3fA-80	Cab-1 m-3f-80		
	Clk-Out/Trig-Out/Extra	200 cm	Cab-1 m-9 m-200	Cab-1 m-9f200	Cab-1m-3mA-200		Cab-1 m-3f-200		
	Information					nominal attenuation of loss cables series Cl	of 0.3 dB/m at 100 MHz and HF		
Low Loss Cables	Order No.	Option							
	CHF-3mA-3mA-200	Low loss cables SMA male to SMA male 200 cm							
	CHF-3mA-9m-200	Low loss of	w loss cables SMA male to BNC male 200 cm						
					on MF141 cables and have an attenuation of 0.3 dB/m at 500 MHz and amended for signal frequencies of 200 MHz and above.				
Software SBenchó	Order no.								
Software Spelicito	SBench6								
SBenchó Base version included in delivery. Supports standard mode for one card. SBenchó-Pro Professional version for one card: FIFO mode, export/import, calculation									
	SBench6-Multi Option multiple cards: Needs SBench6-Pro. Handles multiple synchronized cards in one system.						tom		
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- [1] : Just one of the options can be installed on a card at a time.
 [2] : Third party product with warranty differing from our export conditions. No volume rebate possible.

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